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REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
A	RELEASED ON ECN #E 1563	10/08/03	MA

- 1. Specification subject to change without notice.**
- 2. All dimensions and specifications apply to standard modules. This information may vary for modules with optional features.**
- 3. All dimensions are in millimeters.**
- 4. Precautions: These precautions apply equally to modules from all makers, not just Densitron. Violation of these guidelines may void the warranty and can cause problems ranging from erratic operation to catastrophic display failure.**

Handling precautions:

- ◆ This device is susceptible to Electro-Static Discharge (ESD) damage. Observe Anti-Static precautions.

Power supply precautions:

- ◆ Identify and, at all times, observe absolute maximum ratings for both logic and LC drivers. Note that there is some variance between models.
- ◆ Prevent the application of reverse polarity to VDD and VSS, however briefly.
- ◆ Use a clean power source free from transients. Power up conditions are occasionally "jolting" and may exceed the maximum ratings of the module.
- ◆ The +5V power of the module should also supply the power to all devices which may access the display. Don't allow the data bus to be driven when the logic supply to the module is turned off.
- ◆ DO NOT install a capacitor between the Vo (contrast) pin and ground. VDD must, at all times, exceed the Vo voltage level. The capacitor combines with the contrast potentiometer to form an R-C network which "holds-up" Vo, at power-down, possibly damaging the module.

Operating precautions:

- ◆ DO NOT plug or unplug the module when the system is powered up.
- ◆ Minimize the cable length between the module and host MPU. (Recommended max. length 30 cm).
- ◆ For models with EL backlights, do not disable the backlight by interrupting the HV line. Unloaded inverters produce voltage extremes which may arc within a cable or at the display.
- ◆ Operate the module within the limits of the modules temperature specifications.

Mechanical / Environmental precautions:

- ◆ Improper soldering is the major cause of module difficulty. Use of flux cleaner is not recommended as they may seep under the elastomeric connection and cause display failure. Densitron recommends the use of Kester "245" no-clean solder.
- ◆ Mount the module so that it is free from torque and mechanical stress.
- ◆ Surface of LCD panel should not be touched or scratched. The display front surface is an easily scratched, plastic polarizer. Avoid contact and clean only when necessary with soft, absorbent cotton dampened with petroleum benzene.
- ◆ ALWAYS employ anti-static procedure while handling the module.
- ◆ Prevent moisture build-up upon the module and observe the environmental constraints for storage temperature and humidity.
- ◆ DO NOT store in direct sunlight.
- ◆ If leakage of the liquid crystal material should occur, avoid contact with this material, particularly ingestion. If the body or clothing becomes contaminated by the liquid crystal material, wash thoroughly with water and soap.

Notes: (unless otherwise specified)

Unless otherwise specified: Dimensions are mm Tolerances are: X = ± 3 .X = ± 0.5 .XX = ± 0.05 CAGE CODE #OWS52	APPROVALS	DATE	DENSITRON TECHNOLOGIES PLC	
	DRAWN			
	CHECKED		TITLE	LCD MODULE COB 4 LINE X 20 CHARACTERS
	ISSUED		DWG. NO.	LC2434

1.0 DESCRIPTION

Dot matrix display module consisting of a Liquid Crystal Display, CMOS driver and controller LSI, printed circuit board and metal support frame.

Available LC fluid type is: STN-H (Extended temperature range STN).

2.0 MECHANICAL CHARACTERISTICS

Item	Specifications	Unit
Package Dimensions	98.8 (W) x 60.2 (H) x 11.3 max. (D)	mm
Display format	4 line x 20 characters	-
Character font format	5 (W) x 7 (H) with attached cursor	dots
Driving method	1/16	duty
Dot size	0.55 (W) x 0.55 (H)	mm
Dot pitch	0.60 (W) x 0.60 (H)	mm
Character Size	2.95 (W) x 4.75 (H)	mm
Active display area	70.4 (W) x 20.8 (H)	mm
Viewing area	76.0 (W) x 25.5 (H)	mm
Weight		g

Notes:W-Width;H-Height;D-Depth.

3.0 ABSOLUTE MAXIMUM RATINGS

VSS=0V;Ta=25°C

Item	Symbol	STN-H		Unit
		Min.	Max.	
Logic supply voltage	VDD-VSS	0	7	V
LC driver supply voltage	VDD-VO	0	10	V
Operating temperature	TOP	-20	+70 (Note 3)	°C
Storage temperature (Note 1)	TST	-30	+80	
Humidity: Operating (@40°C)	-	-	85%	RH (Note 2)
Non-operating (@40°C)	-	-	95%	RH (Note 2)

Notes: 1: Tested to 100 hrs.
2: Refers to non-condensing conditions.
3: It is not recommended to operate EL lamp above 50°C.

4.0 ELECTRICAL CHARACTERISTICS

VDD=5±0.25V;Ta=25°C

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input "High" voltage	V _{IH}	-	2.2	-	V _{DD}	V
Input "Low" voltage	V _{IL}	-	-	-	0.6	V
Output "High" voltage	V _{OH}	I _{OH} =0.205mA	2.4	-	-	V
Output "Low" voltage	V _{OL}	I _{OL} =1.2mA	-	-	0.4	V
Power supply current	I _{DD}	V _{DD} =5.0V	-	3	-	mA

5.0 RECOMMENDED LC DRIVE VOLTAGE (V_{DD}-V_o)

V_{DD}=5.0±0.25V

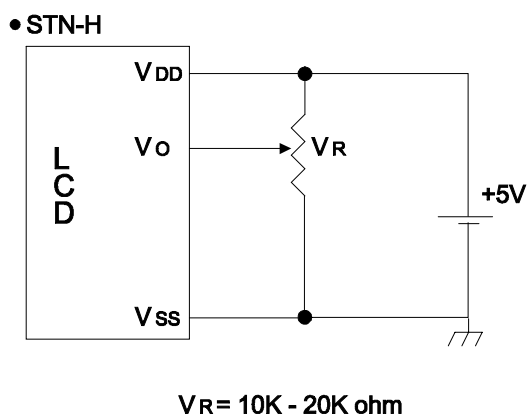
Temperature	STN-H
T _a = -20°C	4.7
T _a = 0°C	4.7
T _a = 25°C	4.6
T _a = 50°C	4.5
T _a =70°C	4.5

6.0 BACKLIGHT SPECIFICATIONS:

T_a=20°C,60%RH,Darkroom.

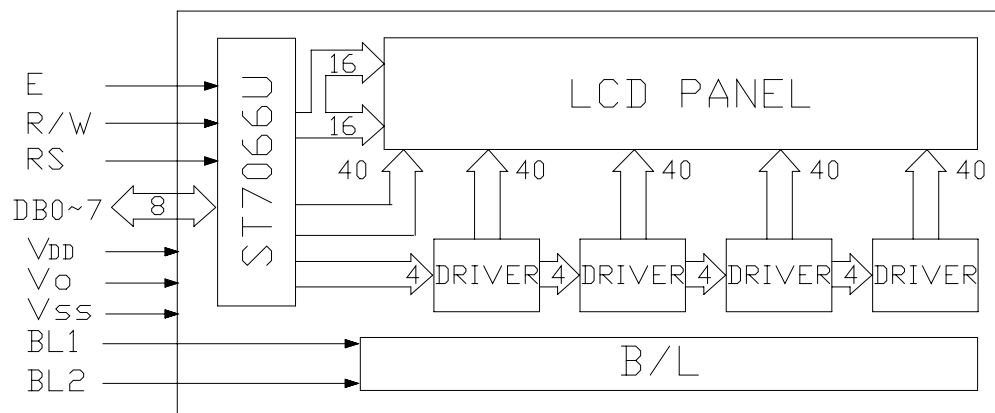
Item	Symbol	Typ.	Max.	Unit
EL lamp input voltage	VEL	100	150	Vrms
EL lamp input current	IEL	3.5	-	mA
EL lamp input frequency	FEL	400	800	Hz
Life to half initial brightness	-	2500	3000	Hours
Recommended EL backlight inverter	-	DAS5V7A	-	-

7.0 POWER SUPPLY



8.0 INTERFACE DESCRIPTION

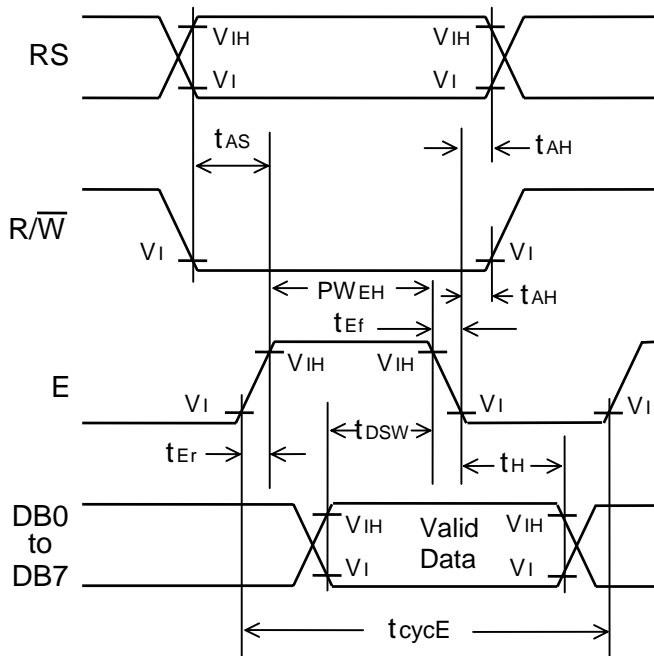
Pin No.	Symbol	I/O	Function
1	Vss	-	Ground (0V)
2	VDD	-	Logic supply Voltage (+5V)
3	Vo	-	LC Drive voltage for contrast adjustment
4	RS	I	Register Select 0: Instruction Register 1: Data Register
5	R/W	I	Read / Write 0: Data Write (Module←MPU) 1: Data Read (Module←MPU)
6	E	I	Enable Signal Active High (H→L)
7	DB0	I/O	Bi-directional data bus line 0
8	DB1	I/O	Bi-directional data bus line 1
9	DB2	I/O	Bi-directional data bus line 2
10	DB3	I/O	Bi-directional data bus line 3
11	DB4	I/O	Bi-directional data bus line 4
12	DB5	I/O	Bi-directional data bus line 5
13	DB6	I/O	Bi-directional data bus line 6
14	DB7	I/O	Bi-directional data bus line 7
15	N/C	-	No Connection
16	N/C	-	No Connection
BL1	VEL	-	EL Backlight input voltage (from output of DC-AC inverter)
BL2	VEL	-	EL Backlight input voltage (from output of DC-AC inverter)



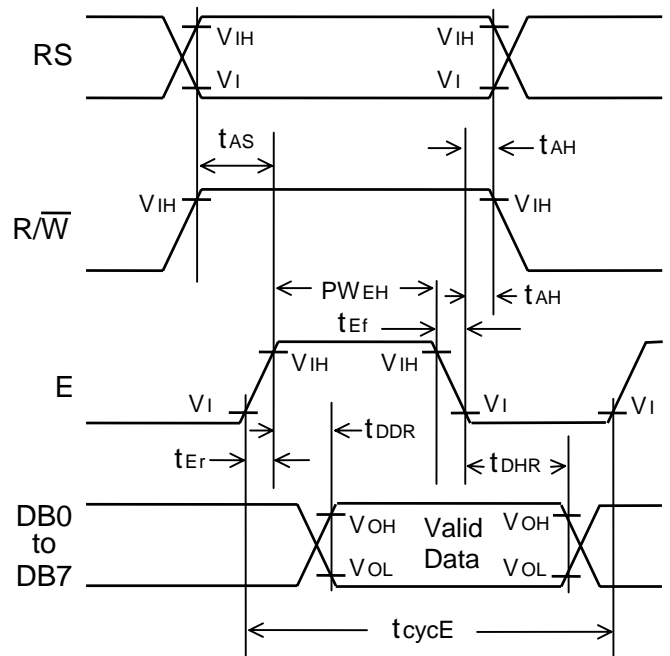
10.0 TIMING CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit
Enable cycle time	T_{cycE}	1.0	-	-	nS
Enable pulse width	PW_{EH}	450	-	-	nS
Enable rise / fall time	t_{Er}/t_{Ef}	-	-	25	nS
Address set-up time	t_{AS}	140	-	-	nS
Address hold time	t_{AH}	10	-	-	nS
Data delay time	t_{DDR}	-	-	320	nS
Data hold time (Write)	t_{DHW}	10	-	-	nS
Data hold time (Read)	t_{DHR}	20	-	-	nS
Data set-up time	t_{DSW}	195	-	-	nS

WRITE OPERATION



READ OPERATION



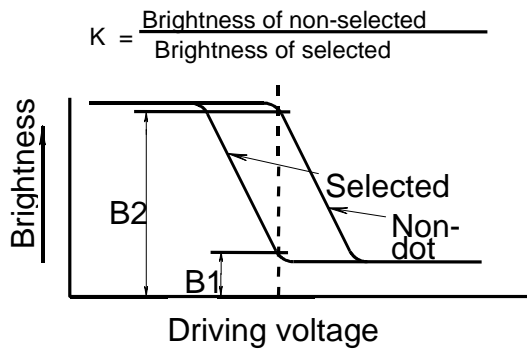
11.0 DD RAM ADDRESS vs. DISPLAY POSITION

Character	1	2	3	4	5	6	7	8	9	10	11	---	18	19	20
Line 1	00	01	02	03	04	05	06	07	08	09	0A	---	11	12	13
Line 2	40	41	42	43	44	45	46	47	48	49	4A	---	51	52	53
Line 3	14	15	16	17	18	19	1A	1B	1C	1D	1E	---	25	26	27
Line 4	54	55	56	57	58	59	5A	5B	5C	5D	5E	---	65	66	67

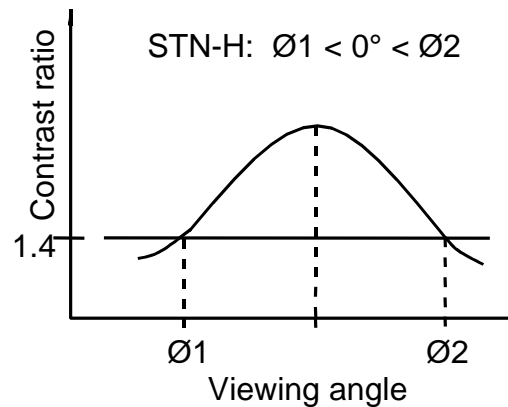
12.0 OPTICAL CHARACTERISTICS

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Contrast ratio STN-H	K	$\varnothing=20^\circ \theta=0^\circ$	4	-	-	-
Viewing angle STN-H	$\varnothing2-\varnothing1$ θ	$\theta=0^\circ K \geq 1.4$ $\varnothing=20^\circ K=1.4$	40 ± 30	-	-	Deg. Deg.
Response time Rise	t_r	$\varnothing=20^\circ \theta=0^\circ$	-	150	250	mS
Fall	t_f	$\varnothing=20^\circ \theta=0^\circ$	-	150	250	mS

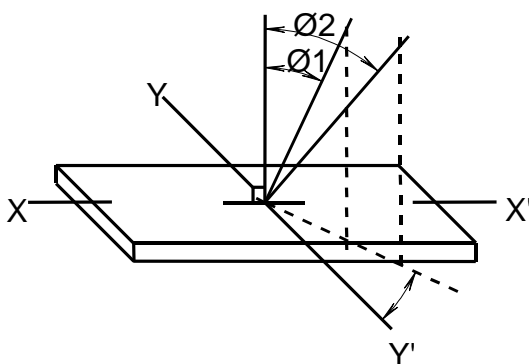
DEFINITION OF CONTRAST RATIO



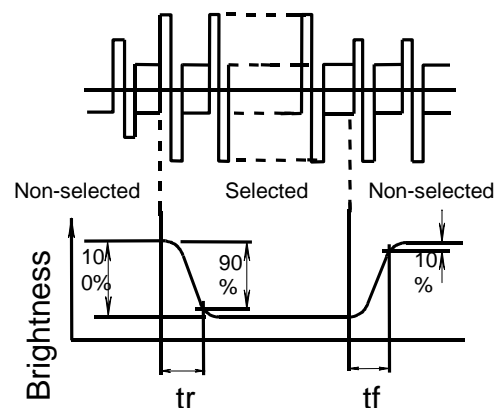
CONTRAST VERSUS VIEWING ANGLE



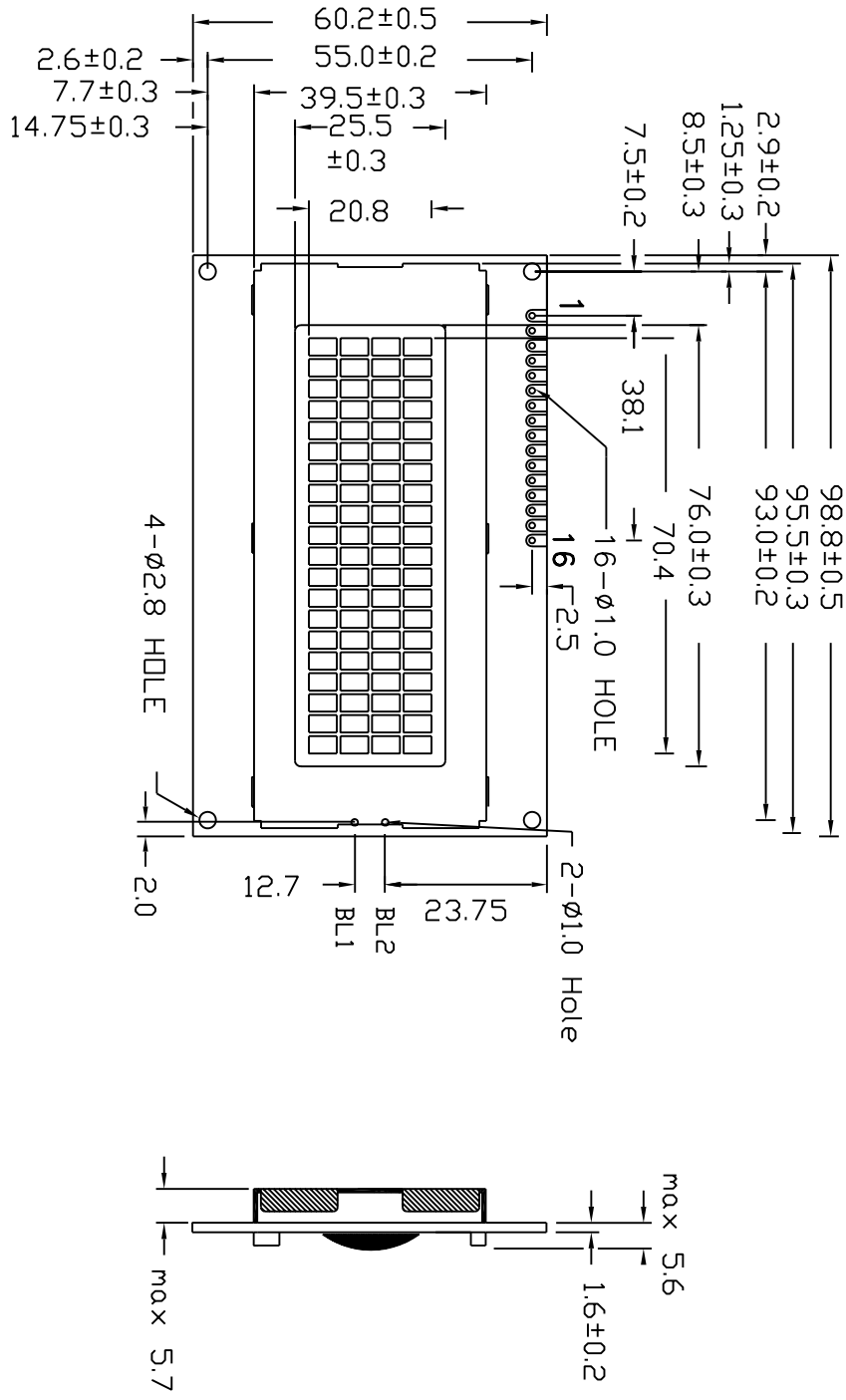
DEFINITION OF ANGLES \varnothing AND θ



DEFINITION OF OPTICAL RESPONSE



13.0 MODULE DIMENSIONS



Cage Code #DWSS52

DWG. NO.

LC2434

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14.0 PART NUMBER DESCRIPTION FOR AVAILABLE OPTIONS

LC2434①②4C20③④⑤

①

Polarizer Type

A = Reflective: light background, no backlight

B = Transflective: light background with blue-green EL backlight

②

Not applicable- LEAVE BLANK

③

Fluid Type and Power Supply

H = STN-H with +5VDC operation

④

Fluid Type Viewing Direction

N = STN-H

⑤

Background Color for STN

G = Gray background

Y = Yellow background